Indian Institute of Information Technology Bhagalpur

Electronics and Communication Engineering (ECE)

B.Tech. Curricula and Syllabus

Semester-II

Curricula:

Course Code	Course name	L	T	P	C
MA102	**Engineering Mathematics – II**	**3**	**1**	**0**	**4**
CS102	**Data Structures and Algorithms**	**3**	**0**	**0**	**3**
EC102	**Digital Design**	**3**	**0**	**0**	**3**
EC104	**Semiconductor Devices and Circuits**	**3**	**0**	**0**	**3**
ME103	**Engineering Materials**	**3**	**1**	**0**	**4**
CS112	**Data Structure and Algorithm LAB**	**0**	**0**	**3**	**2**
EC113	**Digital Design LAB**	**0**	**0**	**3**	**2**
EC114	**Semiconductor Devices and Circuits LAB**	**0**	**0**	**3**	**2**
NSS	**National Service Scheme**	**0**	**0**	**0**	**0**

Syllabus:

Course Code	Course Name	L	T	P	C	Year	Semester
MA102	Engineering Mathematics II	3	1	0	4		
Course Objective:							
Topic							Hour
Module I	Vector functions of one variable – continuity, differentiation and integration. Functions of several variables - continuity, partial derivatives, directional derivatives, gradient, differentiability, chain rule; tangent planes and normals. Concavity-Convexity, Maxima and minima, Saddle Pont, Lagrange multiplier method.						9
Module II	Repeated and Multiple integrals with applications to volume, surface area, Moments of Inertia, change of variables, Vector Fields, Line and Surface Integrals.						8
Module III	Green's, Gauss' and Stokes' theorems and their applications. First order differential equations - exact differential equations, integrating factors, Bernoulli equations, existence and uniqueness theorem, applications.						8
Module IV	Higher-order linear differential equations, solutions of homogeneous and non-homogeneous equations, method of variation of parameters. Series solutions of linear differential equations. Legendre equation and Legendre polynomials. Bessel equation and Bessel functions of first and second kinds.						10
Module V	Systems of first-order equations, two-dimensional linear autonomous system, phase plane, critical points, stability.						8

	Total	43
Text	1. Thomas Calculus; Maurice D Weir, Joel Hass, ; 13th, Pearson India Education Services Pvt.Ltd; 2008. 2. Advanced Engineering Mathematics; Erwin Kreyszig, Herbert Kreyszig, Edward J Norminton; 10th, Wiley India Pvt. Ltd.; 2017. 3. Elementary Differential Equations and Boundary Value Problems; William E Boyce, Richard C DiPrima, ; 9th, Wiley India Pvt. Ltd.; 2009.	
Reference	1. Calculus Early Transcendentals; James Stewart, , ; 7th, Cengage; 2012. 2. A Course in Multivariable Calculus and Analysis; Sudhir R Ghorpade, Balmohan V Limaye, ; 1st, Springer; 2018. 3. Differential Equations; Shepley L Ross, Richard C DiPrima, ; 3rd, Wiley India Pvt. Ltd.; 2004.	

Course Code	Course Name	L	T	P	C	Year	Semester
CS102	Data Structures and Algorithms	3	0	0	3	1st	2nd
Course Objective: A good algorithm usually comes together with a set of good data structures that allow the algorithm to manipulate the data efficiently. In this course, students will get to know various data structures that are used in various computational problems.							
Topic							Hour
Module I	Performance of algorithms: space and time complexity, asymptotics, lower and upper bounds.						7
Module II	Fundamental Data structures: arrays, linked lists, matrices, stacks, queues, binary trees, tree traversals.						7
Module III	Algorithms for sorting and searching: linear search, binary search, insertion-sort, selection sort, bubble sort, quicksort, mergesort, heapsort; Priority Queues: lists, heaps.						7
Module IV	Hashing: separate chaining, linear probing, quadratic probing; Search Trees: binary search trees, B-trees.						6
Module V	Graphs: Data Structures for Graphs, Breadth First Search, Depth First Search.						8
						Total	35
Text	1. Data Structures with C; Seymour Lipschutz, ; 1st, McGraw Hill Education (India) Pvt. Limited; 2011. 2. Introduction to Algorithms; Thomas H Cormen, Charles E Leiserson, Ronald L Rivest; 3rd, PHI Learning Private Limited; 2018.						
Reference	1. Fundamentals of Data Structures in C; Ellis Horowitz, Sartaj Sahni, Susan Anderson; 2nd, Universities Press (India) Private Limited; 2017.						

Course Code	Course name	L	T	P	C	Year	Semester

EC102	Digital Design	3	0	0	3	1st	2nd
Course objective: The main objective of this course is to introduce the number system, elements of digital system abstractions such as digital representations of information, logic gates, combinational and sequential circuits, Boolean algebra, state elements and finite state machine (FSMs).							
Topic	Contents						No. of Lectures
Module-I	Number Systems and Codes, Positional number system, Binary, octal and hexadecimal number systems; Methods of base conversions; Binary, octal and hexadecimal arithmetic; Representation of signed numbers; Fixed and floating point numbers; Binary coded decimal codes; Gray codes; Error detection and correction codes - parity check codes and Hamming code						06
Module-II	Combinatorial Logic Systems: Definition and specification; Truth table; Basic logic operation and logic gates; Boolean Algebra and Switching Functions: Basic postulates and fundamental theorems of Boolean algebra; Standard representation of logic functions - SOP and POS forms; Simplification of switching functions - K-map and Quine-McCluskey tabular methods; Synthesis of combinational logic circuits						10
Module-III	Logic Gates, Two-level realizations using gates -AND-OR, OR-AND, NAND-NAND and NOR-NOR structures; Multifunction gates, Multi-bit adder, Multiplexers, DE-multiplexers, Decoders, Programmable ALU; Multiplexer-based realization of K-maps; Combinational circuit design using multiplexers and gates						09
Module-IV	Sequential Logic systems: Latches and Flip-flops, Timing hazards and races; Analysis of state machines using D flip-flops and JK flip-flops; Synchronous and Asynchronous counters; Registers; Sequence generator using flip-flops; Design of state machines-state table, state assignment, transition/excitation table, excitation maps and equations, logic realization; Design examples						10
Module-V	Memory:Read-only memory, read/write memory-SRAM and DRAM; TTL, MOS, interfacing between logic families; RAM, ROM, PAL, and PLA.						07
Total							42
Text	1. M. Morris Mano, Digital Logic and Computer Design, Pearson Education, 11th edition, 2009. 2. R. P. Jain, Modern Digital Electronics, Tata McGraw Hill, 3rd edition, 2011.						
Reference	1. R. J. Tocci, N. S. Wisdmer and G. L. Moss, Digital Systems: Principle and Applications, Pearson Education, 10th edition, 2011.						

Course Code	Course name	L	T	P	C	Year	Semester
EC104	Semiconductor Devices & Circuits	3	0	0	6	1st	2nd

Course objective: The main objective of this course is to study semiconductor materials and transport mechanism, semiconductor diodes, bipolar transistors, field effect devices and transistors. More particularly, the course objectives are to:

1. Introduce students to the physics of semiconductors and the inner working of semiconductor devices.
2. Provide students the insight useful for understanding new semiconductor devices and technologies.

Topic	Contents	No. of Lectures
Module-I	Introduction of semiconductors, equilibrium and carrier concentration in semiconductors; Bond model and band model of intrinsic semiconductors, Density of state, Fermi-dirac distribution function; Carrier transport in semiconductors, Mobilty, resistivity and conductivity; Excess carrier, method of generating excess carrier inside extrinsic semiconductors. Doping and diffusion process.	08
Module-II	P-N Junction: Simplified device structure and physical operation of diode; depletion region, forward and reverse-bias, depletion and diffusion capacitances, switching characteristics; breakdown mechanisms; Zener diode, Tunnel diode; Diode Applications: Half Wave and Full Wave Rectifier, Clippers and Clampers, and Zener Regulators	09
Module-III	Simplified device structure and physical operation of BJT, I-V characteristics of BJT, carrier distribution; current gain, transit time, secondary effects; SPICE model. Metal-semiconductor junctions, Breakdown of the junction with the non-impact and impact ionization, β-I_C characteristics curve, variation of α with I_c; Small signal equivalent circuit, BJT Amplifiers: Transistor Configuration analysis, Common base, Common emitter and Common collector	08
Module-IV	MOS structure, Energy band diagrams, Flat-band condition and flat-band voltage, Surface accumulation, surface depletion, Threshold condition and threshold voltage, MOS C-V characteristics, MOS Q-V Characteristics.	08
Module-V	Introduction to Field effect transistors, Construction and characteristics of Junction Field effect transistors; N-channel and p-channel JFET characteristics; MOSFETS: Enhancement type and depletion type of MOSFET, Basic Operation and Characteristics; N-channel and P-channel MOSFET characteristics	09
Total		42

Text	1. R. F. Pierret, Semiconductor Device Fundamentals, Pearson Education, 1st edition, 2006. 2. B. G. Streetman and S. K. Banerjee, Solid State Electronic Devices, Pearson Education, 7th edition, 2015.
Reference	1. J. Singh, Semiconductor Devices - Basic Principles, John Wiley & Sons Inc., 1st edition, 2001.

Course Code	**Course name**	**L**	**T**	**P**	**C**	**Year**	**Semester**
ME103	Engineering Materials	3	0	0	3	1st	2nd
Topic	**Contents**						**No. of Lectures**
Module-I	Introduction: Classification of materials; Structure-property Relations; Metals & Alloys, Ceramics, Polymers, Composites and Semiconductors, Atomic Structure & Interatomic Bonding, Fundamentals of Atomic Structure and Chemical Bonding, Atomic Bonding in Solids..						**08**
Module-II	Environmental Degradation of materials: Oxidation and Corrosion, Thermal and Photo Degradation, Chemical Degradation, Radiation Damage.						**08**
Module-III	Structure of solids: Crystalline and Non-crystalline states, Crystallographic directions and phases, Determination of crystal structures.						**08**
Module-IV	Properties of materials: Thermal Properties, Electrical properties, Dielectric behaviour, Magnetic properties, Superconductivity, Optimal properties.						**08**
Module-V	Materials selection: Material properties and Engineering Design parameters, General effects of processing on parameters, selection of structural, Electronic and Magnetic Materials – case studies.						**08**
						Total	**40**
Text	1. L.H. Van Vlack, *Elements of Materials Science & Engineering*, Addison-Wesley Publishing Company, New York. 2. V Raghavan, *Materials Science & Engineering*, Prentice Hall of India Pvt. Ltd., New Delhi.						
Reference	1. W D Callister, Jr., *Materials Science & Engineering – An Introduction,* John Willey & Sons, Inc, New York.						