



PARENT TEACHER ASSOCIATION

COLLEGE OF ENGINEERING AND MANAGEMENT PUNNAPRA

(Under the Co-operative Academy of Professional Education, Estd. by Government of Kerala)

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No. CEMP/PTA/2024/15

Date.....18/03/2024

QUOTATION NOTICE

Sealed competitive quotations are invited from qualified firms/persons for supplying FPGA Trainer Boards to Digital lab under department of Electronics and Communication in College of Engineering and Management ,Punnapra.The last date fixed for receipt of quotations is 01/04/2024 , 2 PM and will be opened at 3 PM on the same day. Detailed specification is given as follows. Ph.0477-2267311.

Sl. No	Item	Specification	Quantity
1	Basys 3 Artix-7 FPGA Trainer Board with USB A to Micro-B Cable	<ul style="list-style-type: none">• 4 Pmod ports: 3 Standard 12-pin Pmod ports, 1 dual purpose XADC signal / standard Pmod port• 4-digit 7-segment display• 5 user pushbuttons• 16 user LEDs• 16 user switches• USB HID Host for mice, keyboards and memory sticks• 12-bit VGA output• USB-UART Bridge• Serial Flash• Free Web PACK™ download for standard use.• Designed Exclusively for Vivado Design Suite. Expanded features are available through purchase of the Design Edition.• Digilent USB-JTAG port for FPGA programming and communication • On-chip analog-to-digital converter (XADC)• Internal clock speeds exceeding 450 MHz• 90 DSP slices• Five clock management tiles, each with a phase-locked loop (PLL) • 1,800 Kbits of fast block RAM	4

- Copy to 1. Block Panchayath Office,Ambalapuzha
2. Village Office ,Paravoor
3.Panchayath office , Punnapra North
4. Notice Board
5. Website



Principal

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