



Er. Akash Abhisek

Plot.No-MIG-2,65/5
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OBJECTIVE

Seeking a suitable position in a leading organization, contributing my skill and experience by developing and executing a strategic long-term vision while leading the organization to achieve measurable results and growth.

PROFESSIONAL EXPERIENCE

Lecturer

Bhadrak Institute of Engineering and Technology

07/2011 – Present

Bhadrak, Odisha, India

Site Supervisor

M/s. General Industrial Builders Pvt. Ltd.(Under L&T ECCD)

06/2009– 06/2011

Dhenkanal, Odisha, India

Photogrammetric Engineer

Assurgent Technology Solutions Pvt. Ltd.

08/2007 – 04/2009

Salt Lake, Kolkata, West Bengal

EDUCATION

PhD *Electrical Engineering*
NIT,Meghalaya *Continuing*

M.Tech *Power System*
Engineering
Biju Patanaik University of Technology *8.33 CGPA*
2018 *Rourkela, Odisha, India*

B.Tech *Electrical Engineering*
Biju Patanaik University of Technology *6.77*
CGPA
2007 *Rourkela, Odisha, India*

12th
Science
Council of Higher Secondary Education
51%
2003 *Bhubaneswar,Odisha, India*

10th
Board of Secondary Education *70.60 %*
2001 *Bhubaneswar, Odisha, India*

TECHNICAL SKILLS

- | | |
|--------------------|---------------------|
| 1. Matlab/SIMULINK | 2. PC E1 Schematic |
| 3. C, C++ | 4. Microsoft Office |

SHORT TERM COURSES

- | | |
|--|---|
| 1. Modeling and Control of Renewable Energy Sources | 04-08 th June, 2012
NIT, Rourkela |
| 2. Recent Trends in Advanced Computing (RTAC-2013) | 29 th April-11 th May, 2013
VSSUT, Burla |
| 3. Recent Advances in Power, Control & Energy | 25 th -26 th April, 2014
NIT, Meghalaya |
| 4. Estimation and Filtering with Applications | 20 th -22 nd Feb, 2015
NIT, Rourkela |
| 5. Power Electronics Application in Sustainable Energy & E-Transportation Systems | 09 th -13 th Sep, 2024
NITTTR, Kolkata |

ACHIEVEMENTS

- | | |
|---|--|
| Professor In Charge of Admission(2020-Present)
<i>BIET, Bhadrak, Odisha, India</i> | Students Co-ordinator of Department(2016 – Present)
<i>BIET, Bhadrak, Odisha, India</i> |
| Co-ordinator for Online Classes(2020-Present)
<i>BIET, Bhadrak, Odisha, India</i> | |

PAPER PUBLISHED

1. Abhishek, A., Biswal, C., Rout, P. K., & Panda, G. (2024). Protection strategy for fault detection in AC microgrid based on MVMD & differential CUSUM. *International Journal of Emerging Electric Power Systems*, (0).
2. Dalai, S. K., Prince, S. K., Abhishek, A., Affijulla, S., & Panda, G. (2022, September). Power management strategies for islanding and grid-connected DC microgrid systems with multiple renewable energy resources. In *2022 IEEE Global Conference on Computing, Power and Communication Technologies (GlobConPT)* (pp. 1-6). IEEE.

M.TECH THESIS DETAIL

- TITLE** : Selected Harmonic Elimination in 5 Level Inverter Using Genetic Algorithm Toolbox For Solar Applications.
- Software** : MATLAB
- Introduction** : The inverter is a power electronic device that converts fixed dc to ac. To reduce the ripple content in the output and to improve the power quality inverters need very high switching frequency and various PWM strategies. In 3-level inverter, output voltage waveform is produced by using PWM

with 3 voltage levels. This has limitations at high voltage which created high dv/dt and increases the stress on the power switches. Because this causes the output voltage and current to be distorted and the THD . Hence multi-level inverters are used.

PERSONAL DETAIL

Father's Name	: Mr. Akshaya Kumar Behera	Marital Status	: Married
Birthday	: July 27, 1986	Nationality	: Indian
Gender	: Male		

Declaration

I, Akash Abhisek, hereby declare that the information contained herein is true and correct to the best of my knowledge and belief.



Akash Abhisek

Bhubaneswar, Odisha